Unijunction Transistor







A PN unijunction transistor in a TO–92 type package designed for use in pulse and timing circuits, sensing circuits and thyristor trigger circuits

Absolute maximum Ratings: (Ta = +25°C unless otherwise specified)



Operating Junction Temperature Range, T_J : -65°C to +125°C Storage Temperature Range, Tstg : -65°C to +150°C



Electrical Characteristics: (T_A = +25°C Unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Instrinsic Standoff Ratio		V _{B2B1} = 10V, Note3	0.56	-	0.75	-
Interbase Resistance	гвв		4	6	9.1	kΩ
Interbase Resistance Temperature Coefficient			0.1	-	0.9	%/°C
Emitter Saturation Voltage	VEB1(sat)	V _{B2B1} = 10V, I _E = 50mA, Note 4	-	2.5	-	V
Modulated interbase Current	IB2(mod)	V _{B2B1} = 10V, I _E = 50mA	-	15	-	mA
Emitter Reverse Current	IEB20	V _{B2E} = 30V, I _{B1} = 0	-	0.005	1	μA
Peak Point Emitter Current	IР	V _{B2B1} = 25V	-	1	5	μΑ
Valley Point Current	lv	V_{B2B1} = 20V, R_{B2} = 100 Ω , Note 4	2	5	-	mA
Base-One Peak Pulse Voltage	V _{OB1}		3	6	-	V

Notes:

- 1. Duty Cycle <= 1% PRR = 10PPS.
- 2. Based upon power dissipation at Ta = +25°C
- 3. Intrinsic standoff ratio is essentially constant with temperature and interbase voltage and is defined by the equation:

Vp - Vbb + Vd

Where: Vp = Peak Point Emitter Voltage; Vbb = interbase Voltage;

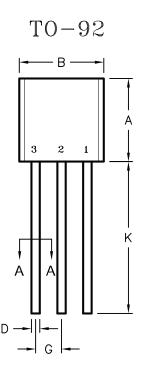
Vd = Junction Diode Drop (~0.5V).

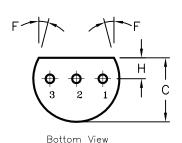
5. Use Pulse techniques: Pulse width $\sim 300 \mu S$, Duty Cycle <= 2% to avoid internal heating due to interbase modulation which may result in erroneous readings



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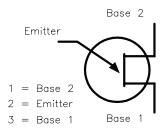


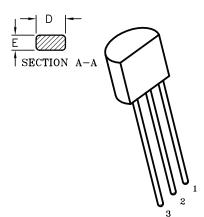




Dim	Min	Max		
Α	4.3	5.33		
В	4.45	5.2		
С	3.18	4.19		
D	0.41	0.55		
Е	0.35	0.5		
F	5	;o		
G	1.14	1.4		
Н	1.14	1.53		
K	12.7	-		

Dimensions : Millimetres





Part Number Table

Description	Part Number		
Unijunction Transistor, TO-92, PN	2N4870		

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